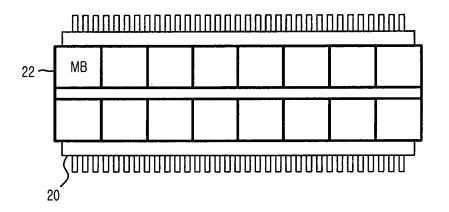
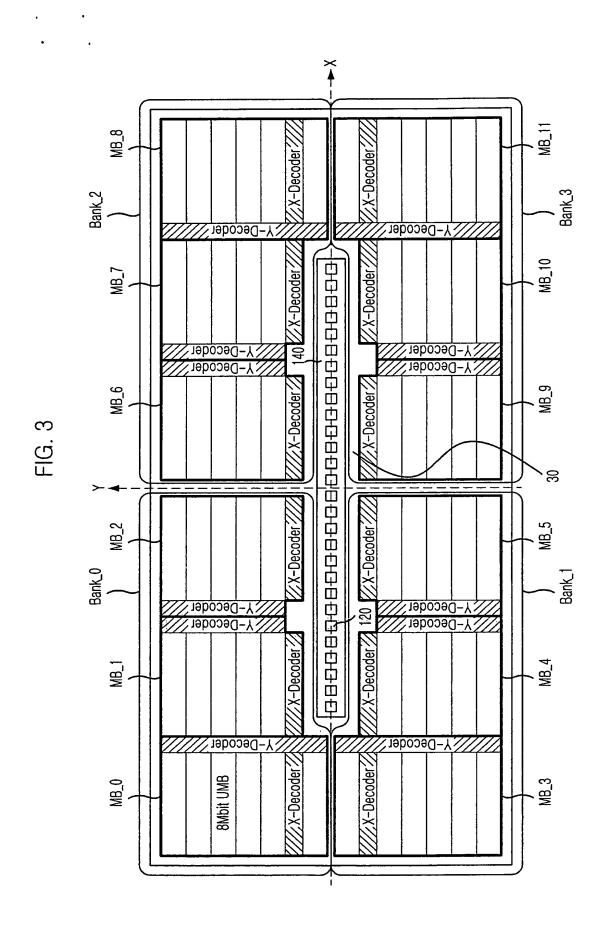
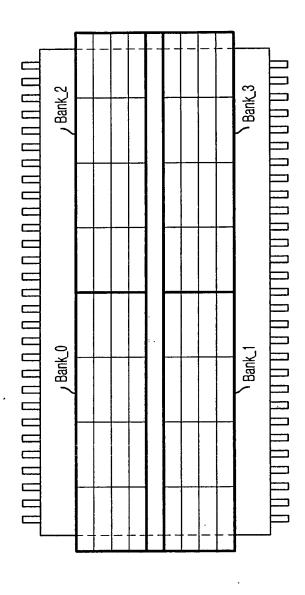


FIG. 2 (PRIOR ART)

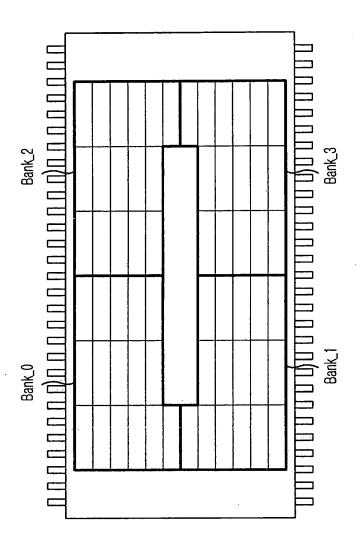




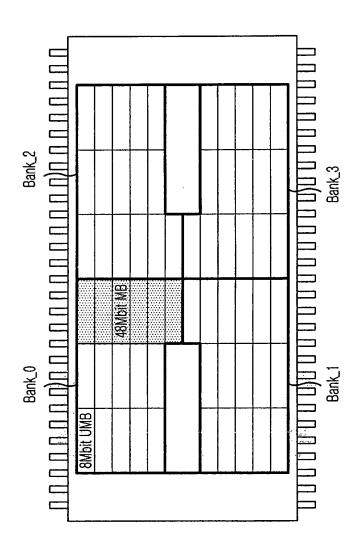


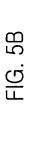


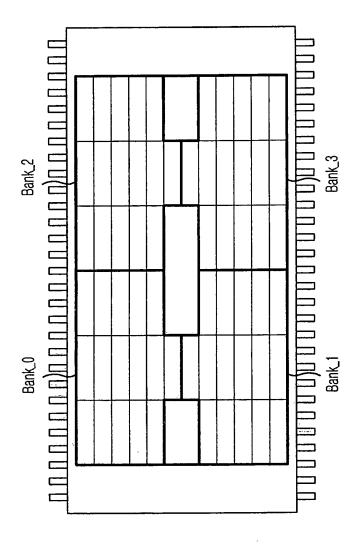






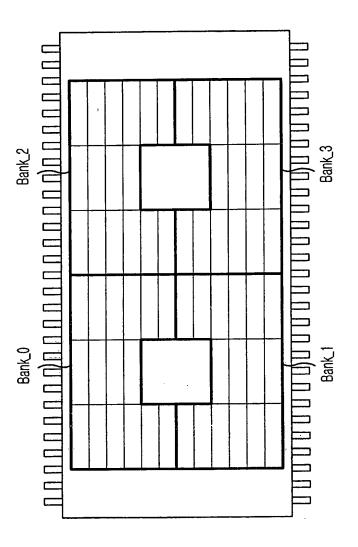




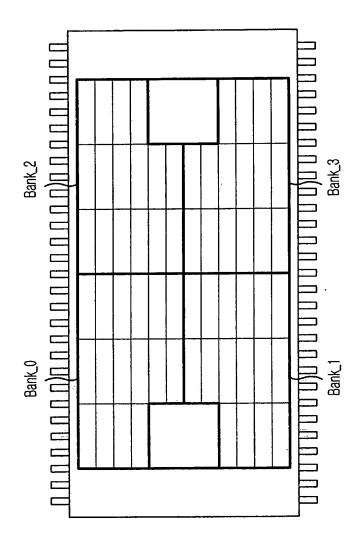


	_										•	
FIG. 6	Bank_2 }	(1,6)	WB	X-Decoder	(2a,6a) MB		(2b,6b) MB	X-Decoder	(3,6)	WB		
		oder	Y-Dec	+	Y-Decoder		Y-Decoder	\Box	ecoder	<u>Λ</u> -D	<u> </u>	
		(1,5)	MB	X-Decoder	[(2,5)	<u>.</u>		X-Decoder	(3,5)	WB		
		oder	y−Dec		Y-Decoder		Y-Decoder		ecoder	O-γ	ļ	
		(1,4)	WB	X-Decoder	(2a,4a) MB	000000000	(2b,4b) MB	X-Decoder	(3,4)	WB	Bank_3	
	· ·			,		. 🗆		T T			7	
		(1,3)	MB	X-Decoder	(2a,3a) MB	00000000	(2b,3b) MB	X-Decoder	(3,3)	MB		
		oder	Ͻ ϴ Ω−ϒ		Y-Decoder		Y-Decoder		есодец	Λ-D]	
		(1,2)	MB	X-Decoder	 	000		X-Decoder	(3,2)	MB	·	
		oder	o9U−Y		Y-Decoder	$ \ $	Y-Decoder		эсодег	<u>λ-D</u>	4	
	Bank_0 \	(1,1)	32Mbit MB	X-Decoder	(2a,1a) 16Mbit MB		(2b,1b) MB	X-Decoder	(3,1)	WB	Bank_1	
						[1					
						PAD						









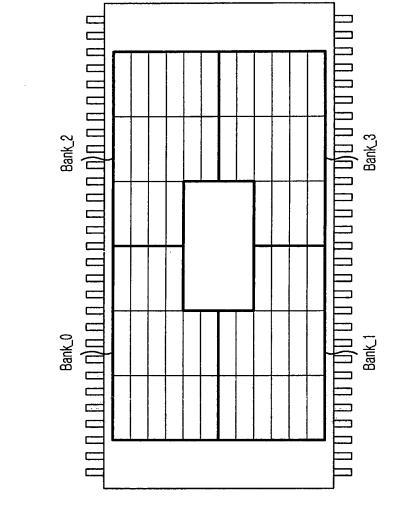
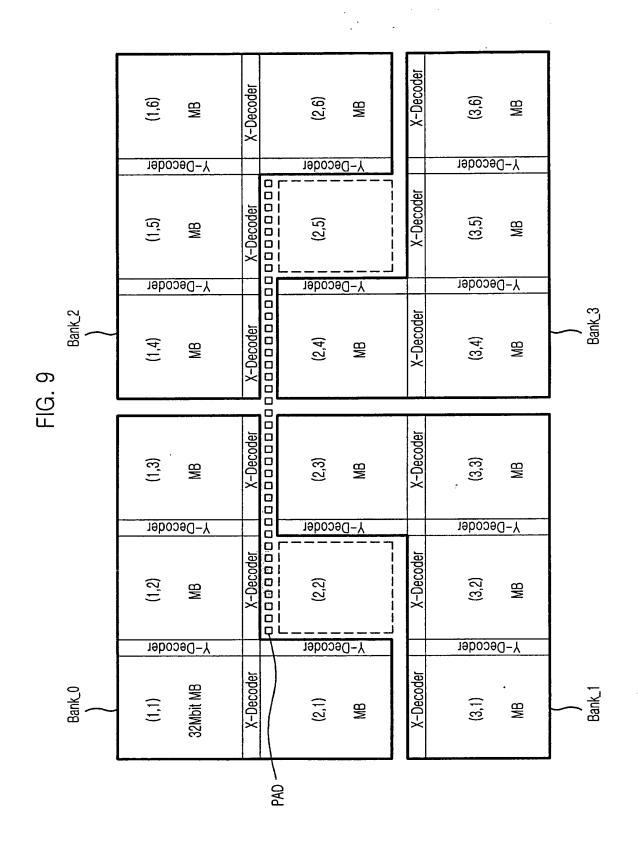
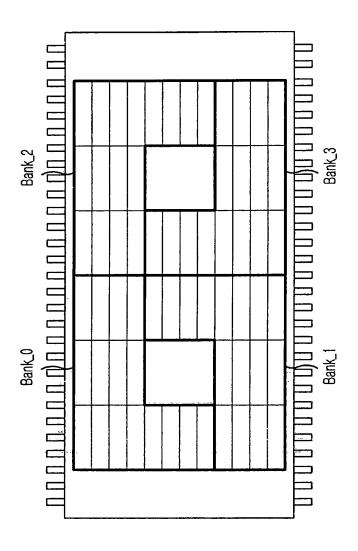
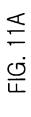


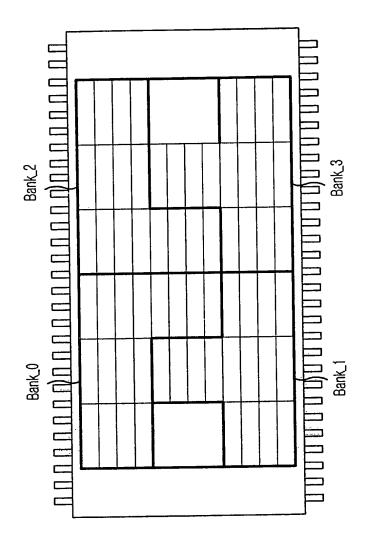
FIG. 8B













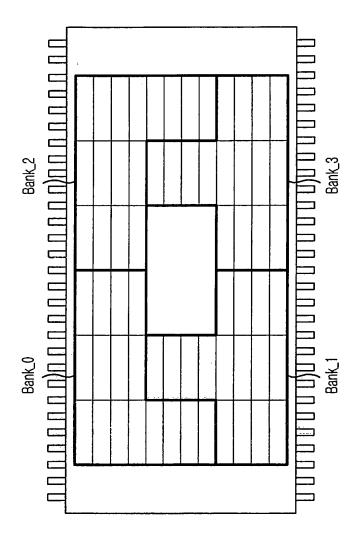


FIG. 12

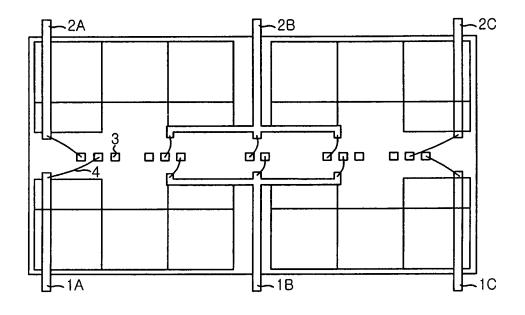


FIG. 13

